

SRI VELLAPALLY NATESAN COLLEGE OF ENGINEERING

Affiliated to the APJ Abdul Kalam Technological University (KTU), Thiruvananthapuram
Approved by AICTE, New Delhi and Accredited by NAAC



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SRI VELLAPPALLY NATESAN COLLEGE OF ENGINEERING

Affiliated to APJ Abdul Kalam Technological University, Thiruvanathapuram
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INTERNAL QUALITY ASSURANCE CELL (IQAC)

ANNUAL REPORT

AY 2018-19

Introduction

This Annual Report serves as a comprehensive overview of the activities and accomplishments undertaken by the Internal Quality Assurance Cell (IQAC) at Sri Vellappally Natesan College of Engineering during the academic year 2018-19. In alignment with our commitment to excellence, the report details strategic initiatives, progress made, and prospects as we strive to maintain and elevate the academic standards of our institution

1. Improving Research Facility:

Our institution took significant steps to cultivate a thriving research atmosphere on campus. Recognizing the importance of continuous improvement in research, the Internal Quality Assurance Cell (IQAC) coordinator was actively instructed to implement measures that enhanced our research capabilities. This initiative underscored our commitment to fostering a culture of innovation and academic excellence.

2. Review of IEDC Functions:

In alignment with our dedication to nurturing entrepreneurship, the Principal directed a comprehensive review of the functions of the Innovation and Entrepreneurship Development Center (IEDC). Emphasis was placed on increasing activities under IEDC, with a specific focus on motivating our students to explore and create new start-ups. This initiative aimed to instil an entrepreneurial mindset among our student body.

3. Updation of NAAC Files:

The Annual Quality Assurance Report (AQAR) for the previous academic year was completed. The meticulous updation of National Assessment and Accreditation Council (NAAC) files was a testament to our commitment to maintaining high academic standards and continuous improvement. This accomplishment reflected our dedication to transparency and accountability in the accreditation process.

4. Preparation for Academic Year 2018-2019:

The Principal provided clear guidance by instructing the Head of the Department for Computer Science and Engineering (HoD-CSE) to lead the preparation and publication of the academic calendar. This proactive approach ensured a smooth and well-organized start to the upcoming academic session, promoting effective planning and communication.

V. Principal

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5. Increasing Placement Opportunities:

To enhance our commitment to providing valuable career opportunities for our students, we initiated strategic steps to attract more companies to our campus. The development of a comprehensive action plan for the placement cell, which was successfully prepared and presented, was a crucial component of our efforts. This initiative aimed to enhance our students' employability and strengthen our industry connections.

6. Conducting Academic Audit:

To ensure the highest standards of academic excellence, we formed an audit committee tasked with conducting a thorough academic audit. The date for the audit was finalized, reflecting our commitment to accountability and continuous improvement. This initiative was instrumental in maintaining the quality and relevance of our academic programs.

7. Student Feedback Collection:

Monitoring the pulse of our academic community, we followed up on the status of feedback collection from students. Heads of Departments (HoDs) were actively engaged in overseeing and ensuring the timely completion of this essential feedback collection process. This initiative underscored our commitment to listening to and addressing the needs of our student body.

8. Introduction of Skill-Oriented Courses:

Recognizing the importance of equipping our students with practical skills, we initiated efforts to encourage enrollment in Additional Skill Acquisition Programme (ASAP) courses and other value-added courses. HoDs were instructed to explore possibilities and promote the uptake of these skill-oriented courses, aligning our curriculum with industry demands.

9. Result Improvement:

Acknowledging the imperative to enhance academic outcomes, faculty members were directed to take proactive measures to support slow learners and improve overall results. This initiative emphasized our commitment to personalized student support and academic success.

10. Use of ICT Tools:

Recognizing the transformative potential of Information and Communication Technology (ICT), HoDs were assigned the responsibility of directing faculty members to increase the incorporation of ICT tools in their classes. This initiative aimed to enhance the quality of teaching and learning, aligning our practices with contemporary educational standards.

11. Workshops and Seminars:

Encouraging a culture of continuous learning and knowledge dissemination, departments were actively encouraged to organize more workshops and seminars. The Head of the Department for

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Electrical and Electronics Engineering (HOD-EEE) was assigned the responsibility of coordinating these events, ensuring a diverse range of educational opportunities for our students

Prepared by

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Mr. Sujith S. Pillai Coordinator - IQAC Approved By

Dr. H Ganesan Principal

TALEDO.

Principal Mahaguru Institute of Technology Kattachira, Pallickal P.O.- 690503 Kayamkulan